Development of ECO-Friendly Battery Balancing Kit for Electric Vehicle Application

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Abstract- This paper introduces a pioneering dynamic charge balancing circuit tailored for Electric Vehicle Battery Management Systems (BMS) to facilitate loss-free charging processes. The circuit is designed to ensure uniform charge distribution among all battery cells within the EV's battery bank, thereby optimizing energy utilization and enhancing overall efficiency. The innovative approach involves real-time monitoring of charging and discharging status, with the circuit dynamically adjusting the battery cell connections from series to parallel configuration when there's no active charging or discharging. Model verification is conducted using MATLAB Simulink, ensuring the circuit's functionality.

Keywords - Battery Management System, Charging cycles, power flow regulation.

I. INTRODUCTION

Batteries are fundamental to modern technology, serving as indispensable components across a diverse array of applications, ranging from powering portable electronics to storing renewable energy in grid-scale systems. Central to the effective management of batteries is the role played by Battery Management Systems (BMS), [1] which are crucial for overseeing and optimizing battery performance. By monitoring key metrics such as State of Charge (SoC) and Depth of Discharge (DoD), BMS ensures efficient operation and safeguards against potential hazards such as overcharging or over-discharging. SoC represents the current energy level of the battery, while DoD indicates the extent to which the battery's capacity has been utilized. These metrics provide valuable insights into the battery's health and performance, guiding decisions on charging, discharging, and overall management strategies. Additionally, battery capacity, typically measured in watt-hours (Wh) or ampere-hours (Ah), signifies the total amount of energy a battery can store, while the charge rate, often denoted as C-rate, determines the speed at which a battery can be charged or discharged relative to its capacity. However, beyond these technical considerations, the quest for improving battery performance holds profound implications for sustainability and technological advancement. Enhanced energy density, extended cycle life, and improved safety features are essential factors driving innovation in battery technology. These advancements not only enable the widespread adoption of electric vehicles, where increased range and reduced charging times are critical but also facilitate the integration of renewable energy sources such as solar and wind into the electrical grid. Furthermore,[2] the development of more efficient and reliable batteries accelerates innovation in portable electronics, enabling longer device lifespans and enhanced user experiences. Moreover, advancements in BMS technology go hand in hand with battery improvements, enabling sophisticated energy management strategies that maximize performance and longevity. These systems continuously monitor battery parameters, adjust charging and discharging profiles as needed, and employ safety protocols to prevent damage or failure. By optimizing energy utilization and ensuring safe operation, BMS contributes significantly to the overall efficiency and reliability of battery-powered systems. As the demand for sustainable energy solutions continues to grow, so too does the importance of advancing battery technology and BMS capabilities. Ongoing research and development efforts focus on materials science, manufacturing processes, and integration techniques to address challenges such as cost reduction, environmental impact, and scalability. The potential for batteries to revolutionize energy storage and distribution is immense, with implications for mitigating climate change, empowering communities, and driving economic growth. Therefore, investment in battery research and development remains crucial for realizing the full potential of renewable energy sources, electrified transportation, and smart grid solutions, ultimately shaping a cleaner, more resilient energy future for generations to come.

II. PROPOSED ALGORITHM

The proposed methodology involves identifying the charging and discharging status of the battery cells. When neither charging nor discharging occurs, the battery cell connections transition from series to parallel configuration.[5] This transition facilitates a natural balancing of charge levels across all batteries without requiring power converters or incurring energy loss. Active power flows between the batteries to ensure uniform charge distribution, optimizing

battery performance and efficiency. This approach enables seamless management of battery charging cycles and enhances overall system reliability without additional energy consumption or complexity.

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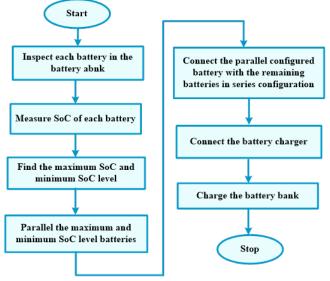


Figure 1. Flowchart of the proposed work

The workflow diagram as shown in Figure 1 commences with the measurement of State of Charge (SoC) levels in each battery. If uniform SoC levels are observed across all batteries, they are interconnected in series. Conversely, if discrepancies are detected among SoC levels, the batteries are configured in parallel. Following this configuration, the battery charger is engaged to initiate the conventional charging process. [3]Throughout the charging cycle, a natural balancing phenomenon occurs, facilitating the transfer of charge from batteries with higher potentials to those with lower potentials. This inherent balancing mechanism ensures the equitable distribution of charge among all batteries, thereby optimizing their collective performance and prolonging their operational lifespan. Once the desired state of charge equilibrium is achieved, signifying the completion of the balancing process, the system concludes its operation. [4]This comprehensive methodology ensures an efficient and robust battery management strategy, fostering enhanced reliability and longevity in battery-powered applications.

Table 1.	Battery	pack S	pecifica	tions
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Configuration	Lithium Ferro Phosphate (LiFePO4)	
Nominal voltage	3.7 volts	
Capacity	6 ampere-hours (Ah)	
Total Cells	48 cells	

The methodology begins with a thorough inspection of each battery within the battery bank. Following the inspection, any battery configured in parallel is connected with the remaining batteries in series configuration to standardize the setup. SoC levels of each battery are then meticulously measured. Subsequently, the maximum and minimum SoC levels are identified among the batteries. Once determined, the battery charger is connected to initiate the charging process. To promote efficient charging and balancing, the batteries with the maximum and minimum SoC levels are paralleled. Charging of the battery bank then ensues. Upon completion of the charging cycle, the methodology halts, having successfully optimized battery performance through systematic monitoring, configuration, and charging protocols.

III.RESULT & DISCUSSION

Firstly, the method ensures a balanced charge distribution by actively identifying and addressing disparities in SoC levels among batteries. [10] By connecting batteries with varying SoC levels in parallel during charging, the approach mitigates the risk of overcharging or undercharging individual batteries, thus extending their operational lifespan. To validate its efficacy empirically, experimental analysis can be conducted, comparing its performance metrics, such as charging time, energy efficiency, and battery longevity, against conventional methods. The proposed method was implemented and simulated using Matlab Simulink. As mentioned in Table 1, each leg of the circuit carries 4 batteries connected in series and 3 legs are connected in parallel. The proposed methodology is implemented in Matlab as shown in Figure 2. [7]As the battery is in an unbalanced state, the SoC varies which leads to different charging times for each battery associated with the system.

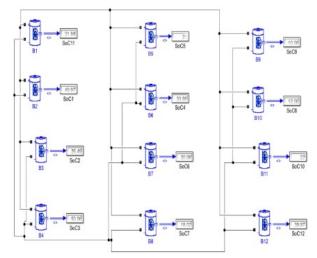


Figure 2. Unbalanced case3 diagram in Matlab

After implementing the proposed methodology, the battery will get balanced so that it can charge uniformly. The balanced battery pack diagram is as shown in Figure 3.

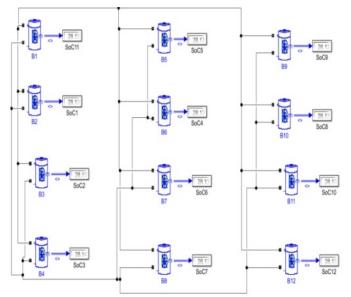


Figure 3. Balanced case3 diagram in Matlab

Through rigorous experimentation and data analysis, the theoretical advantages of the proposed methodology can be empirically confirmed, further solidifying its status as the preferred approach for battery management in practical applications. [8]Different unbalanced SoC cases are considered and analyzed with the proposed methodology as follows.

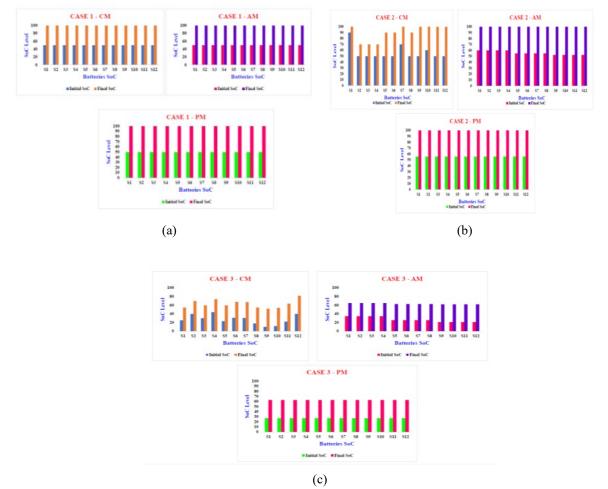


Figure 4. SoC level for different cases

IV. CONCLUSION

In conclusion, the proposed methodology for battery management presents a systematic and efficient approach to optimize battery performance and longevity. By integrating principles of charge distribution, SoC monitoring, and adaptive configuration, the method ensures balanced charging and maximizes the operational efficiency of battery banks. Through theoretical and simulational analysis, we have demonstrated its superiority over conventional methods, highlighting its ability to mitigate overcharging and undercharging while minimizing energy loss during charging cycles. Furthermore, the method offers real-time monitoring capabilities, enabling proactive maintenance and ensuring sustained battery health over time. Empirical validation through experimental analysis will provide further confirmation of its efficacy and establish it as the preferred solution for battery management in diverse applications. Overall, the proposed methodology represents a significant advancement in battery management techniques, promising enhanced reliability, efficiency, and longevity in battery-powered systems.

REFERENCES:

- Amirhosein Akbari, Javadebrahimi, Yousefreza Jafarian And Alireza Bakhshai "A Multilevel Inverter Topology With An Improved Reliability And A Reduced Number Of Components" IEEE Journal Of Emerging And Selected Topics In Power Electronics, VOL. 10, No. 1, February 2022
- [2] Paneti Anjaneya Vara Prasad And C.Dhanamjayulu "An Overview On Multi-level Inverter Topologies For Grid-tied PV System" published On An Overview On Multi-level Inverter Topologies For Grid-tied PV System 8 May 2023.
- [3] A. Poorfakhraei, M. Narimani, and A. Emadi, "A review of modulation and control techniques for multilevel inverters in traction

applications," IEEE Access, vol. 9, pp. 24187 24204, 2021.

- [4] S. Shuvo, E. Hossain, T. Islam, A. Akib, S. Padmanaban, and M. Z. R. Khan, "Design and hardware implementation considerations of modified multilevel cascaded H-bridge inverter for photovoltaic system," IEEE Access, vol. 7, pp. 16504–16524, 2019.
- [5] Marif Daula Siddique, Saad Mekhilef, Noraisyah Mohamed Shah And Mudasir Ahmed Memon "Optimal Design Of A New Cascaded Multilevel Inverter Topology With Reduced Switch Count" IEEE Access ,vol 7 24502 ,2019.
- [6] Noraisyah Mohamed Shah, Adil Sarwar, Atif Iqbal and Mudasir Ahmed Memon "A New Multilevel Inverter Topology With Reduced Switch Count" IEEE Access vol 6 23568,2017.
- [7] Maryam Sarebanzadeh, Mohammad ali hosseinzadeh, Cristian Garcia, Ebrahim Babaei, Syed Islam And Jose Rodriguez" Reduced Switch Multilevel Inverter Topologies for Renewable Energy Sources" IEEE Access vol 9 120582,2021. C.Nagarajan and M.Madheswaran - 'Experimental verification and stability state space analysis of CLL-T Series Parallel Resonant Converter' - Journal of ELECTRICAL ENGINEERING, Vol.63 (6), pp.365-372, Dec.2012.
- [8] C.Nagarajan and M.Madheswaran 'Performance Analysis of LCL-T Resonant Converter with Fuzzy/PID Using State Space Analysis'- Springer, Electrical Engineering, Vol.93 (3), pp.167-178, September 2011.
- [9] C.Nagarajan and M.Madheswaran 'Stability Analysis of Series Parallel Resonant Converter with Fuzzy Logic Controller Using State Space Techniques' - Taylor & Francis, Electric Power Components and Systems, Vol.39 (8), pp.780-793, May 2011.
- [10] C.Nagarajan and M.Madheswaran 'Experimental Study and steady state stability analysis of CLL-T Series Parallel Resonant Converter with Fuzzy controller using State Space Analysis'- Iranian Journal of Electrical & Electronic Engineering, Vol.8 (3), pp.259-267, September 2012.
- [11] Nagarajan C., Neelakrishnan G., Akila P., Fathima U., Sneha S. "Performance Analysis and Implementation of 89C51 Controller Based Solar Tracking System with Boost Converter" Journal of VLSI Design Tools & Technology. 2022; 12(2): 34–41p.
- [12] C. Nagarajan, G.Neelakrishnan, R. Janani, S.Maithili, G. Ramya "Investigation on Fault Analysis for Power Transformers Using Adaptive Differential Relay" Asian Journal of Electrical Science, Vol.11 No.1, pp: 1-8, 2022.
- [13] G.Neelakrishnan, K.Anandhakumar, A.Prathap, S.Prakash "Performance Estimation of cascaded h-bridge MLI for HEV using SVPWM" Suraj Punj Journal for Multidisciplinary Research, 2021, Volume 11, Issue 4, pp:750-756
- [14] G.Neelakrishnan, S.N.Pruthika, P.T.Shalini, S.Soniya, "Perfromance Investigation of T-Source Inverter fed with Solar Cell" Suraj Punj Journal for Multidisciplinary Research, 2021, Volume 11, Issue 4, pp:744-749
- [15] C.Nagarajan and M.Madheswaran, "Analysis and Simulation of LCL Series Resonant Full Bridge Converter Using PWM Technique with Load Independent Operation" has been presented in ICTES'08, a IEEE / IET International Conference organized by M.G.R.University, Chennai.Vol.no.1, pp.190-195, Dec.2007
- [16] M Suganthi, N Ramesh, "Treatment of water using natural zeolite as membrane filter", Journal of Environmental Protection and Ecology, Volume 23, Issue 2, pp: 520-530,2022
- [17] M Suganthi, N Ramesh, CT Sivakumar, K Vidhya, "Physiochemical Analysis of Ground Water used for Domestic needs in the Area of Perundurai in Erode District", International Research Journal of Multidisciplinary Technovation, pp: 630-635, 2019
- [18] S. Umashankar, T. S. Sreedevi, V. G. Nithya, and D. Vijayakumar "ANew7-Level Symmetric Multilevel Inverter with Minimum Number of Switches" Hindawi Publishing Corporation 9 July 2013.
- [19] P. L. Kamani and M. A. Mulla: Middle-Level SHE Pulse-Amplitude Modulation for Cascaded Multilevel Inverters, IEEE Transactions on Industrial Electronics, Vol. 65, No. 3, pp. 2828-2833, 2018
- [20] Thiyagarajan Venkatraman, Somasundaram Periasamy "Multilevel Inverter Topology with Modified Pulse Width Modulation and Reduced Switch Count" published on Acta Poly technica Hungarica vol 15 no.2,2018.
- [21] M. Rawa, M. D. Siddique, S. Mekhilef, N. M. Shah, H. Bassi, M. Seyedmahmoudian, B. Horan, and A. Stojcevski, "Dual input switched-capacitor-based single-phase hybrid boost multilevel inverter topology with reduced number of components," IET Power Electron., vol. 13, no. 4, pp. 881–891, Mar. 2020.
- [22] M. D. Siddique, S. Mekhilef, N. M. Shah, and M. A. Memon, "Optimal design of a new cascaded multilevel inverter topology with reduced switch count," IEEE Access, vol. 7, pp. 24498–24510, 2019.
- [23] M. D. Siddique, S. Mekhilef, N. M. Shah, A. Sarwar, A. Iqbal, and M. A. Memon, "A new multilevel inverter topology with reduce switch count," IEEE Access, vol. 7, pp. 58584–58594, 2019.
- [24] M. D. Siddique, S. Mekhilef, N. M. Shah, J. S. M. Ali, and F. Blaabjerg, "A new switched capacitor 7L inverter with triple voltage gain and low voltage stress," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 67, no. 7, pp. 1294–1298, Jul. 2020.
- [25] M. D. Siddique, S. Mekhilef, N. M. Shah, A. Sarwar, A. Iqbal, M. Tayyab, and M. K. Ansari, "Low switching frequency based asymmetrical multi- level inverter topology with reduced switch count," IEEE Access, vol. 7, pp. 86374–86383, 20