

Step-Up DC-DC Converters Using Zero-Voltage Switching Boost Integration Technique in PV and All Renewable Battery Systems

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n-This paper describes the non-isolated high step-up DC-DC converter using Zero Voltage Switching (ZVS) Bi-directional Integration Technique (BIT). The DC-DC converter which integrates PV and battery power for high step up applications are proposed. Coupled inductors are used to attain the high step up voltage gain and also used to reduce the voltage stress of input side switches. The operation need not to be changed when the transition between charging and discharging occurs. It is also capable of operating simultaneously in tracking the maximum power from the sun and generates the output. This implies the usage of PV panel most of the time.

I. INTRODUCTION

The general approach to the application is a classical boost converter which is simple in its structure and gives continuous input current and also provides the clamped switch voltage stress to the output voltage. The integrated multiport converters for interfacing several power sources and energy storage elements are widely used in the recent years. Instead of using separate power electronic converters for every energy sources, multiport converters have few advantages such as usage of fewer amounts of components, lower cost, compact in size and dynamic performance is better. In several conditions atleast one storage device should be used.

A coupled inductor employed in a boost converter is a suitable for step-up technique for its compact structure. It is also possible to achieve a high efficiency and protect switch from the high peak voltage and auxiliary circuit is required to suppress the switch voltage stress and it is also capable of achieving a high soft switching performance. Alternative way of easy approach for a high step up ratio is the current fed type converters with the usage of a transformer. The transformer leakage inductance cause the voltage spikes across the switches so the snubber circuit is needed which results in the additional losses. In view of the clamp capacitors are connected to the input side so that it has no function of extending the step-up ratio. On focusing the step-up ratio extension with the concept of stacked output capacitors, the high step up boost flyback converter is proposed and improved with the secondary voltage-double rectifier. Instead their high step-up capability, the switch suffers from the hard and severe switching losses.

Various types of topologies are proposed due to the advantages of the multiport converters. The combination techniques for the multiport converter include sharing of switches, capacitors, inductors or magnetic cores. It is

important to select a proper topology by considering several

Aspects which includes cost, reliability and flexibility depending on its applications. An application of hybrid energy supply using renewable energy resources and storage devices is shown in Fig. 1.

The DC micro grid enabled by the solid-state transformer (SST) in the Future Renewable Electric Energy Delivery and Management System (FREEDM System) which integrates various distributed renewable energy resources (DESDs). For certain instance, if solar power is selected as the renewable energy source and battery as the storage device. The battery can either supply load with solar energy at the same time or it also stores the excess power from the solar panels for backup purpose. Many multiport converter topologies are implemented in the literature and it can be divided into two categories. One is non-isolated type- the non-isolated converters are usually derived from the typical buck, boost, or buck–boost topologies and are more compact in size. The other is isolated type -the isolated converters using bridge topologies and multi-winding transformers to match wide input voltage ranges.

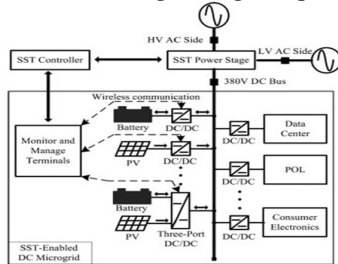


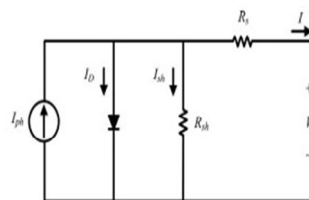
Fig. 1 Part of the FREEDM system diagram showing an SST-enabled DC microgrid.

A topology based on buck configuration is used in airplanes and space-craft front-end system. The battery port in this converter is unidirectional, so the battery cannot be charged from the photovoltaic (PV) port. A multi-input buck–boost type converter is proposed to interface many renewable energy sources but there is no bidirectional port to interface the battery. A two-input converter for a hybrid fuel cell (FC)/battery power system is implemented with zero voltage switching (ZVS) feature. Although the efficiency is improved, this converter could not provide a high voltage conversion ratio and bidirectional functionality. A multiple-input converter based on a boost topology is presented in that has lower input current ripple and therefore is suitable for the large current applications such as hybrid vehicles. Thevenin's theorem is not valid for the non-linear model, but that non-linear model is represented by the linear with non-constant parameters. For example, the piece-wise linearization is used to linearize the diode. The parameters in Fig. 2(a) can be estimated using the manufacturer's datasheet [39]. As shown in Fig. 2(b), the actual diode characteristic has been divided into three regions and the characteristic in each region is approximated as a straight line. Each line can be further represented by a set of voltage source $V_{x,n}$ and resistance $R_{D,n}$ ($n = 1,2,3$). The approximation of piecewise linearization would be more accurate as the number of regions increased. At the boundary points of regions, the values of linearized characteristic are exactly the same as actual characteristic. Therefore, the maximum power point of the PV generator is chosen as one of the boundary points such that the operation at this point has no approximation error. The single-diode model of the PV generator with linearized diode is shown in Fig. 2(c), where the diode is approximated by the voltage source V_x , and resistance R_D . The values of V_x and R_D are dependent on the operation region of the PV generator. The Thevenin's equivalent model of Fig. 2(c) is shown in Fig. 2(d).

II. OPERATION OF THE TOPOLOGICAL MODES:

Before performing the analysis, some assumptions should be made:

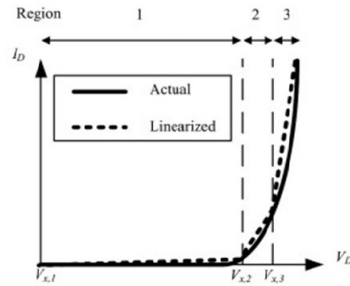
- 1) The switches are assumed to be ideal;
- 2) The magnetizing inductors are large enough so that the current flowing through the inductors are constant;
- 3) The capacitors are large enough so that the voltages across the capacitors are constant.



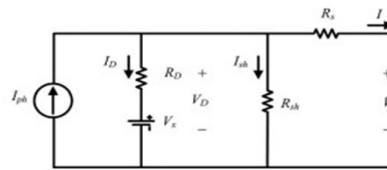
(a) Fig. 2. Thevenin's equivalent circuit derived from the single-diode model. (a) Single-diode model of a PV generator.

III. ZERO VOLTAGE BOOST INTEGRATION TECHNIQUE (ZVS-BIT):

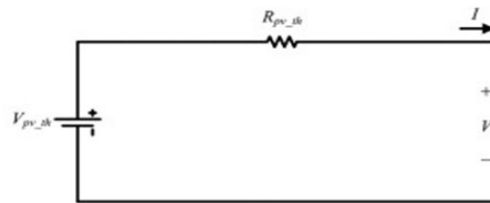
Fig. 3 shows the conceptual diagram of ZVS BIT. A series output module is connected to a bidirectional boost converters a PISO configuration. This series output module can be any isolated current-fed type converter. Also, it can adopt any kind of secondary receivers such as the full-bridge, center-tapped, voltage-doublers, and so on.



(b) V-I characteristics of diode: actual and linear approximation



(c) Single-diode model with linearized diode.



(d)Thevenin's equivalent circuit for a single-diode model with linearized diode.

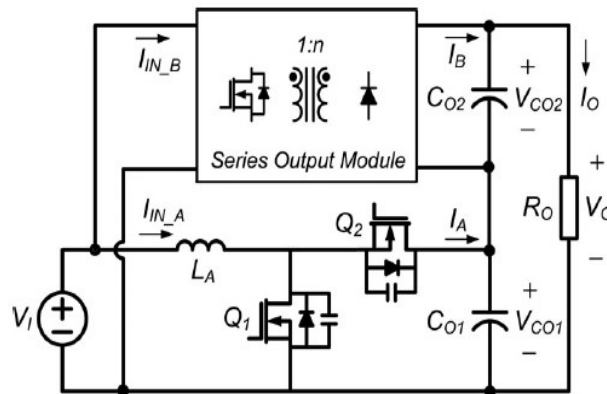


Fig. 3 Conceptual diagram of ZVS-BIT.

High Device Utilization:

To increase the device utilization and simplify the circuit, the common parts integration between a series output module and a bidirectional boost converter is recommended. A flyback converter can be integrated easily since the boost inductor L_A and switch Q_1 in Fig. 1 become the flyback converter counterparts.

High Step-up Capability:

Due to the stacked output capacitors, each output capacitor voltage is added and the overall output voltage can be extended as

$$V_O = V_{CO1} + V_{CO2} \quad (1)$$

That is, the output voltage of the converters employing the proposed ZVS BIT is a sum of the output voltages of the

boostconverter and the series output module, which is suitable for high step-up applications.

Power and Thermal Stress Distribution:

$$nI_{pri}(t) = I_{sec}(t) \quad (3)$$

That is, the current distribution ratio at the powering phase only depends on the transformer turns ratio n , which decides the slope of the primary current $I_{pri}(t)$ while Q_1 is turned off. The powering current distribution depicted by the diagonal-lined and vertical-lined areas satisfies (2) and (3).

Switch Voltage Stress Clamping:

The switch voltage stresses V_{Q1} and V_{Q2} can be clamped to the partial output voltage V_{CO1} . Thus, additional protection circuit for the high voltage spike caused by a leakage inductor L_{lkg} is not required.

Soft Switching Capability:

The proposed ZVS BIT seems to be an active-clamp network, which is not connected to the input side but the output side. The previous works regarding the active-clamp circuit concentrate on the soft switching capability and switch voltage stress clamping [10]–[12], [17]–[21]. However, the proposed ZVS BIT provides these abilities as well as the step-up ratio extension because of the series output connection of the clamp capacitor.

The soft switching characteristics are similar to the active-clamp circuit. A main switch Q_1 has a load-dependent ZVS condition relying on the value of the leakage inductor L_{lkg} , whereas an auxiliary switch Q_2 has a wide ZVS load range resulting from a large boost inductor L_m . Thus, all switches can be turned on under ZVS condition if the energy stored in L_{lkg} is sufficient.

However, since ZVS condition of Q_1 is load-dependent and lost at a light load due to the insufficient leakage energy, the efficiency degradation caused by switching loss is inevitable.

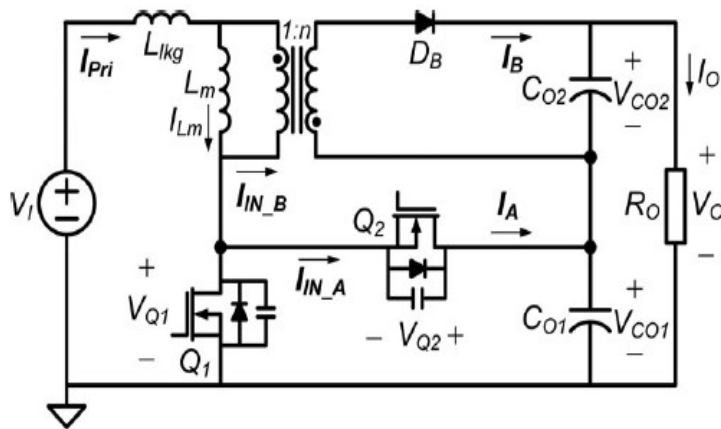


Fig. 4 ZVS fly back boost converter

Comparison of DC-DC boost converters:

In order to satisfy the stringent requirements with performance in renewable energy grid-connected power applications, many researchers' concentrate on how to realize high voltage gain step-up, low cost and high efficiency single-stage converters [12]. The brief comparison of available single-stage transformer less converters will be presented below. They provide the voltage gain up to 20 using coupled inductors or switched capacitor technique. Usually the efficiencies of high voltage gain step-up converters are at the levels over 90% at sub kilowatt or single kilowatt powers. To increase the overall efficiency of converter softswitching technique [8] as well as active clamped circuit introduction may be considered. Boost converter. The single phase single switch boost converter is a basic step-up topology [32]. The voltage gain theoretically is infinite when duty cycle reaches 1. But switch turn on period becomes long as the duty cycle (D) increases causing conduction losses to increase.

The power rating of single switch boost converter is limited to switching rate. In order to obtain higher gain several boost converters can be cascaded at the expense of efficiency decrease. Inter-leaved parallel topology is the solution to increase the power and reduce input current ripple allowing lower power rated switches to be used.

IV. SOFTWARE DESCRIPTION:

MATLAB is a high-level language and interactive environment for numerical computation, visualization, and programming. Using MATLAB, you can analyze data, develop algorithms, and create models and applications. The language, tools, and built-in math functions enable you to explore multiple approaches and reach a solution faster than with spreadsheets or traditional programming languages, such as C/C++ or Java

MATLAB is a program that was originally designed to simplify the implementation of numerical linear algebra routines. It has since grown into something much bigger, and it is used to implement numerical algorithms for a wide range of applications. The basic language used is very similar to standard linear algebra notation, but there are a few extensions that will likely cause you some problems at first.

The goal of the tutorials here is to provide a simple overview and introduction to MATLAB. The tutorials are broken up into some of the basic topics. The first includes a few examples of how MATLAB makes it easy to create and manipulate vectors. The tutorials move from the simple examples and lead to more complicated examples.

V. RESULTS AND DISCUSSIONS:

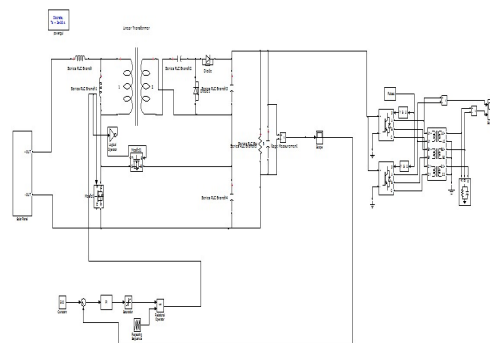


Fig. 6 Entire simulation diagram of DC-DC converter with grid connected system.

Figure 6 shows the experimental set-up of the DC-Dc converter which is feed the input by solar panel and the output of the converter is inverted and connected to the grid system or residential loads. PI controller is used for the performance of the closed loop operation. Three phase inverter is used for the inversion of output voltage from boost converter with the implementation of ZVS BIT technique. The boosting value is high in the ratio of 1:10 in the output by the connection of linear transformer which reduces the high switching losses.

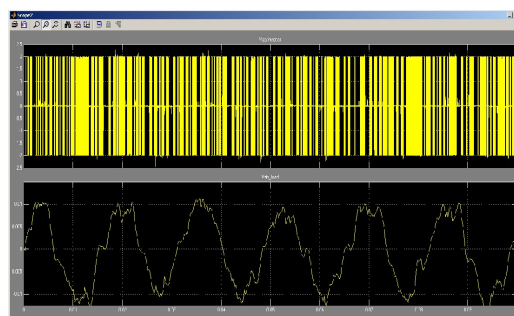


Fig.7 Output waveform of the inverter

The boost converter gives the high range of output when compared to the input voltage from the solar panel and dc source. The inverter output having some ripples because of the electronic components in the circuit.

VI. CONCLUSION AND FUTURE WORK:

Non-isolated high step-up DC-DC converters using ZVS BIT is presented in this paper. The proposed ZVS BIT integrates the common parts between a bidirectional boost converter and a series output module. The simple but effective ZVS BIT gives many desirable features for high efficiency and high step-up applications. With the concept

of ZVS BIT, a ZVS flyback-boost converter with VDR is derived and investigated in detail as an example. All these works are verified experimentally with a digitally controlled prototype converter. Although the efficiency improvement by ZVS BIT is prominent in a ZVS load range, it does not appear at light load condition. Therefore, the proposed ZVS BIT control can be a promising solution for high step-up applications. In the proposed topology, two coupled inductors are employed as voltage gain extension cells for high voltage output applications. Two sets of buck-boost type active-clamp circuits are used to recycle the energy stored in the leakage inductors and improve the efficiency. The proposed switching strategy only needs to control two duty ratios in different operation modes. A high step-up three-port DC-DC converter for stand-alone power systems is proposed to integrate solar and battery power. In light-load condition, once the charging voltage is higher than the present level, the operation mode will be changed rapidly to protect the battery from overcharging. The highest converter efficiency is measured as 90.1% at 110W. The control method of the battery port could be modified for the grid-connected applications.

REFERENCES

- [1] Q. Zhao and F. C. Lee, "High-efficiency, high step-up DC-DC converters," *IEEE Trans. Power Electron.*, vol. 18, no. 1, pp. 65–73, Jan. 2003.
- [2] S. H. Choung and A. Kwasinski, "Multiple-input DC-DC converter topologies comparison," in *Proc. 34th Annu. Conf. IEEE Ind. Electron.*, 2008, pp. 2359–2364.
- [3] L. Solero, A. Lidozzi, and J. A. Pomilio, "Design of multiple-input power converter for hybrid vehicles," in *Proc. IEEE Appl. Power Electron. Conf.*, 2004, pp. 1145–1151.
- [4] C. Nagarajan and M. Madheswaran - 'Stability Analysis of Series Parallel Resonant Converter with Fuzzy Logic Controller Using State Space Techniques'- *Taylor & Francis, Electric Power Components and Systems*, Vol.39 (8), pp.780-793, May 2011.
- [5] S.-K. Han, H.-K. Yoon, G.-W. Moon, M.-J. Youn, Y.-H. Kim, and K.-H. Lee, "A new active clamping zero-voltage switching PWM current fed half-bridge converter," *IEEE Trans. Power Electron.*, vol. 20, no. 6, pp. 1271–1279, Nov. 2005.
- [6] C. Nagarajan and M. Madheswaran - 'Performance Analysis of LCL-T Resonant Converter with Fuzzy/PID Using State Space Analysis'- *Springer, Electrical Engineering*, Vol.93 (3), pp.167-178, September 2011.
- [7] J A Gow, C D Manning "Development of a PV Array model for use in Power Electronics Simulation Studies." *proceedings of the IEEE International Conference on Power Electronics Applications*, vol. 146, March 1999, pp. 193-200.
- [8] Y.M. Chen, Y.C. Liu, S.C. Hung, and C.S. Cheng, "Multi-Input Inverter for Grid-Connected Hybrid PV/Wind Power System," *IEEE Transactions on Power Electronics*, vol. 22, May 2007.
- [9] C. Nagarajan and M. Madheswaran - 'Experimental verification and stability state space analysis of CLL-T Series Parallel Resonant Converter' – *Journal of ELECTRICAL ENGINEERING*, Vol.63 (6), pp.365-372, Dec.2012.
- [10] Nagarajan C., Neelakrishnan G., Akila P., Fathima U., Sneha S. "Performance Analysis and Implementation of 89C51 Controller Based Solar Tracking System with Boost Converter" *Journal of VLSI Design Tools & Technology*. 2022; 12(2): 34–41p.
- [11] C. Nagarajan, G. Neelakrishnan, R. Janani, S. Maitihili, G. Ramya "Investigation on Fault Analysis for Power Transformers Using Adaptive Differential Relay" *Asian Journal of Electrical Science*, Vol.11 No.1, pp: 1-8, 2022.
- [12] G. Neelakrishnan, K. Anandhakumar, A. Prathap, S. Prakash "Performance Estimation of cascaded h-bridge MLI for HEV using SVPWM" *Suraj Punj Journal for Multidisciplinary Research*, 2021, Volume 11, Issue 4, pp:750-756
- [13] G. Neelakrishnan, S.N. Pruthika, P.T. Shalini, S. Soniya, "Performance Investigation of T-Source Inverter fed with Solar Cell" *Suraj Punj Journal for Multidisciplinary Research*, 2021, Volume 11, Issue 4, pp:744-749